

AMENDMENTS

In the Specification:

Please amend Paragraph [0004] as follows:

Further, if a system change renders a TLB translation pair inaccurate, then the inaccurate translation pair is often explicitly purged so that the processor will not attempt to access memory based on the inaccurate translation pair. For example, the page table may be updated, thereby making a translation that is currently stored in the TLB obsolete. This translation pair may be purged from the TLB in response to the page table update update.

Please amend Paragraph [0043] as follows:

FIG. 5 illustrates [[and]] an exemplary architecture and functionality of the execution unit 302 during normal operation. The execution unit 302 continues to operate normally, as indicated in step 516, until a purge signal is received. If a purge signal is received from the system bus 322 (FIG. 3), as indicated in step 502, the execution unit 302 stalls retirements and accesses to the TLB 306 (FIG. 3), as indicated in step 504.